**Location:** Oslo, Norway  
**Duration**: Up to 8 weeks

**What we do at OMNIVISION**

A global fabless semiconductor organization that develops advanced digital imaging, analog and touch & display solutions for multiple applications and industries. OMNIVISION develops and delivers advanced imaging solutions to a variety of industrial and consumer markets including automotive, medical, security & surveillance, computing, mobile phone, and emerging technology spaces.

**What we do at OMNIVISION, Norway**

World-class innovation that evolves with the ever-advancing automotive market. We at OMNIVISION Norway specialize in CMOS Image Senor (CIS) design for the automotive market to cater to the ever increasing demand of CIS in automobiles for applications such as rear view, surround view, E-mirrors, driver monitoring, in-cabin monitoring, ADAS and autonomous driving cameras. The team in Norway have expertise in the areas of analog/digital circuit design & verification, characterization, test & automation, application engineering, safety analysis and project management.

**What we do at OMNIVISION, Norway, Digital design group**

Design and verification of timing and control signals for CMOS pixel array readout & analog/digital converters; image signal processing (ISP) functions such as black level compensation (BLC), defect pixel correction (DPC), lens correction, and high dynamic range processing; serial and parallel I/O interfaces (e.g. I2C, MIPI) for use in OMNIVISIONS’s Image Sensors. As the designs are targeted for automotive market, there is also focus on design for safety. The team handle all tasks of chip development cycle including product specification, architecture definition, RTL design, verification, synthesis, DFT, STA, gate-level-simulation, power analysis & optimization, FPGA emulation, chip bring-up, validation & debug.

**About this position**

As a Digital Design Intern, you will be working with the digital design group on RTL design, verification and/or silicon validation for our on-going projects.

**Responsibilities**

* Design and verification of digital IP for CIS
* Silicon validation and debug
* Safety verification for automotive design
* Scripting for design/verification automation
* Documentation of tasks performed

**Qualifications**

* Currently pursuing a Master’s Degree in Electrical Engineering with preferably completion of 4th year of study
* Experience in RTL design and verification by simulation
* Excellent command of English as a working language

**To apply, please email** [**Norway-jobs@ovt.com**](mailto:Norway-jobs@ovt.com) **and attach the following documents:**

* A copy of your B.Sc. transcript and/or M.Sc. (if a 5 year programme)
* A transcript showing what you have completed so far in your M.Sc.
* CV
* A cover letter outlining your motivation for the position